

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	BIPOLAR JUNCTION TRANSISTOR AND FABRICATING METHOD				
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Anchor Chen</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: NAUP0486USA4</p>					
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td></tr><tr><td>Winston Hsu Registered Number: 41,526</td><td>Winston Hsu</td></tr></table>		Submitted By:	Elec. Sign.	Winston Hsu Registered Number: 41,526	Winston Hsu
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Documents being submitted:	Files
us-fee-sheet	NAUP0486-4-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-request	NAUP0486-4-usrequ.xml us-request.dtd us-request.xsl
application-body	NAUP0486-4-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Naup0486usa4-01.TIF Naup0486usa4-02.TIF Naup0486usa4-03.TIF Naup0486usa4-04.TIF Naup0486usa4-05.TIF Naup0486usa4-06.TIF Naup0486usa4-07.TIF Naup0486usa4-08.TIF Naup0486usa4-09.TIF Naup0486usa4-10.TIF Naup0486usa4-11.TIF Naup0486usa4-12.TIF Naup0486usa4-13.tif Naup0486usa4-14.tif
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